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<u>L101</u>	((determin\$3 or if) near shar\$3) with (cpu or procesor or node)	92	<u>L101</u>
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<u>L99</u>	(determin\$3 or if) near shar\$3 with (exclusive\$2 or writ\$3)	52	<u>L99</u>
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<u>L92</u>	coheren\$2 and L91	116	<u>L92</u>
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<u>L90</u>	(risc or (load near lock or store near conditional)) and L89	452	<u>L90</u>

<u>L89</u>	(conver\$6 or transform\$5 or chang\$3 or modif\$6 or alter\$3) near (exclus\$3 or shar\$3)	19466	<u>L89</u>
<u>L88</u>	(conver\$6 or transform\$5 or chang\$3 or modif\$6 or alter\$3) near (exclus\$3 or shar\$3)	16864	<u>L88</u>
<u>L87</u>	coheren\$2 and L86	104	<u>L87</u>
<u>L86</u>	@pd<20010808 and L85	162	<u>L86</u>
<u>L85</u>	L82 and L84	394	<u>L85</u>
<u>L84</u>	(risc or (load near lock or store near conditional)) and L83	1961	<u>L84</u>
<u>L83</u>	(conver\$6 or transform\$5 or chang\$3 or modif\$6 or alter\$3) with (exclus\$3 or shar\$3)	91007	<u>L83</u>
<u>L82</u>	L76 or L81	26154	<u>L82</u>
<u>L81</u>	(712/41).ccls.	147	<u>L81</u>
<u>L80</u>	(risc or (load near lock or store near conditional)) and L79	62	<u>L80</u>
<u>L79</u>	@pd<20010808 and L78	283	<u>L79</u>
<u>L78</u>	coheren\$2 and L77	800	<u>L78</u>
<u>L77</u>	((displac\$5 or replac\$6) near cach\$3) and L76	1713	<u>L77</u>
<u>L76</u>	L26 or L28 or L29 or L74 or L73 or L72	26018	<u>L76</u>
<u>L75</u>	L26 or L28 or L29 or L74	6534	<u>L75</u>
<u>L74</u>	(707/201).ccls.	1552	<u>L74</u>
<u>L73</u>	(707/8 707/9 707/10).ccls.	8980	<u>L73</u>
<u>L72</u>	(711/117 711/118 711/119 711/120 711/121 711/122 711/123 711/124 711/125 711/126 711/127 711/128 711/129 711/130 711/131 711/132 711/133 711/134 711/135 711/136 711/137 711/138 711/139 711/140 711/141 711/142 711/143 711/144 711/145 711/146 711/147 711/148 711/149 711/150 711/151 711/152 711/153).ccls.	12167	<u>L72</u>
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<u>L68</u>	(return or flush\$3 or displac\$7 or evict\$3 or eject\$3) near (data or block to page or memory) with (shar\$3 or read)	5288	<u>L68</u>
<u>L67</u>	remain\$3 near sharer	3	<u>L67</u>
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<u>L65</u>	((read near "with" (modif\$6 or write)) with (store near instruction))	1976	<u>L65</u>
<u>L64</u>	((before near store near instruction) with (invalid\$5 or fail\$3))	5	<u>L64</u>
<u>L63</u>	((displac\$6 or remov\$3 or expel\$5 or eject\$3 or evict\$3) near (data or memory or copy) with (load near instruction))	20	<u>L63</u>
<u>L62</u>	L31 and L59	9	<u>L62</u>
<u>L61</u>	lock and L60	80	<u>L61</u>
<u>L60</u>	L57 and L59	125	<u>L60</u>
<u>L59</u>	(707/8 707/9 707/10).ccls.	8980	<u>L59</u>
<u>L58</u>	L31 and L57	133	<u>L58</u>
<u>L57</u>	((exclusive or writ\$6) near ((owner) or (copy or memory or data or block)) with shar\$3)	5718	<u>L57</u>

<u>L56</u>	((no or "not") near (exclusive or writ\$6) near owner)	10	<u>L56</u>
<u>L55</u>	L49 and L54	6	<u>L55</u>
<u>L54</u>	L30 and L52	268	<u>L54</u>
<u>L53</u>	L31 and L52	13	<u>L53</u>
<u>L52</u>	((exclusive or writ\$6) near (copy or block or memory or data)) with (load\$3 or stor\$3) near instruction)	1801	<u>L52</u>
<u>L51</u>	((invalid\$5 or fail\$3) near (load\$3 or stor\$3) near instruction)	82	<u>L51</u>
<u>L50</u>	L31 and L49	22	<u>L50</u>
<u>L49</u>	send\$3 near (victim or shar\$3)	919	<u>L49</u>
<u>L48</u>	((writ\$5 or exclusive) with (load\$3 or stor\$3) near instruction) with (shar\$3 or victim))	78	<u>L48</u>
<u>L47</u>	victim adj to adj shar\$3	0	<u>L47</u>
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<u>L45</u>	L30 and L44	33	<u>L45</u>
<u>L44</u>	((displac\$6 or remov\$3 or expel\$5 or eject\$3 or evict\$3) near (data or memory or copy) or (writ\$4 near back)) with (compar\$6 or match\$3 or determin\$3 or test\$3) near address)	127	<u>L44</u>
<u>L43</u>	((displac\$6 near (data or memory or copy) or writ\$4 near back) with (compar\$6 or match\$3 or determin\$3 or test\$3) near address)	107	<u>L43</u>
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<u>L41</u>	write near lock\$3	2242	<u>L41</u>
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<u>L38</u>	L31 and L37	9	<u>L38</u>
<u>L37</u>	((replace\$6 or displac\$8) near (copy or memory or block) with shar\$3)	133	<u>L37</u>
<u>L36</u>	L34 and L35	3	<u>L36</u>
<u>L35</u>	(program or thread or task or process) near concurrent\$3	8642	<u>L35</u>
<u>L34</u>	(read\$3 and writ\$3) and L33	256	<u>L34</u>
<u>L33</u>	directory and L32	270	<u>L33</u>
<u>L32</u>	L30 and L31	342	<u>L32</u>
<u>L31</u>	distributed adj shared adj memory and (cache near (manage\$7 or coheren\$3))	618	<u>L31</u>
<u>L30</u>	L26 or L28 or L27 or L29	15263	<u>L30</u>
<u>L29</u>	(719/312 719/313 719/314).ccls.	973	<u>L29</u>
<u>L28</u>	(718/104 718/108).ccls.	1710	<u>L28</u>
<u>L27</u>	(711/117 711/118 711/119 711/120 711/121 711/122 711/123 711/124 711/125 711/126 711/127 711/128 711/129 711/130 711/131 711/132 711/133 711/134 711/135 711/136 711/137 711/138 711/139 711/140 711/141 711/142 711/143 711/144 711/145 711/146 711/147 711/148 711/149).ccls.	10742	<u>L27</u>
<u>L26</u>	(709/212 709/213 709/214 709/215 709/216).ccls.	2471	<u>L26</u>
<u>L25</u>	victim and L24	68	<u>L25</u>
<u>L24</u>	sharer and L23	191	<u>L24</u>
<u>L23</u>	cach\$3 with (coheren\$2 or synchroniz\$4)	10855	<u>L23</u>

<u>L22</u> sharer and (risc or load adj lock)	12	<u>L22</u>
<i>DB=TDBD,DWPI,JPAB,EPAB,USOC,USPT,PGPB; PLUR=YES; OP=OR</i>		
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<u>L19</u> RAMEY-CARL!	5	<u>L19</u>
<u>L18</u> MATTINA-MATTHEW-C!	6	<u>L18</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
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<u>L16</u> @pd<20010808 and L15	4702	<u>L16</u>
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<u>L6</u> ((requesting or owner or home) near (processor or node)) with (load near lock or risc)	3	<u>L6</u>
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<u>L1</u> (risc or load near (lock or instruction)) with (writ\$3 or exclusive\$2)	1773	<u>L1</u>

END OF SEARCH HISTORY